## **EC Declaration of Conformity**

Manufacturer:

Mitsubishi Electric Corporation, Nagoya Works

Address:

1-14, 5-chome, Yada-Minami, Higashi-ku,

Nagoya, 461 Japan

Products:

Type: Programmable Logic Controller

(Open Type equipment, Installation category II)

Model: QnAS Series

(Applicable units identified in Appendix)

These products comply with the following European directives:

Directive	Name
89/336/EEC	Electromagnetic Compatibility
92/31/EEC	Directive
93/68/EEC	2.130000

Further details of conformity to these directives are contained in the appendices (BCN-P9108).

Authorized Signature:

A. Sugiyama

Manager, FA System Department

Date

23.1.1998

Authorised Representative:

Mitsubishi Electric Europe BV

in the European Community

Gothaer Strasse 8, Postfach 1548,

through Responsible person

D4030, Ratingen 1, Germany

Signature:

Peter Mischitz

Manager, Technical Marketing, Factory Automation

Date:

5.1.1818

The appendices are part of this declaration. This declaration certifies the conformity with the directives mentioned, but does not contain any warranted qualities. The installation, usage and safety directions of the product documentation have to be observed.

## **Appendix**

**QnAS-Series Programmable Logic Controllers** 

Range of products:

A1S38HBEU	4	A1SJ71QC24-R2	4	A1SJ71QE71N-B5T	4	A1SJ72QLR25	4
A1SJ61QBT11	4	A1SJ71QE71-B2	2	A1SJ71QE71N-T	4	Q2ASCPU	4
A1SJ71QBR11	4	A1SJ71QE71-B5	2	A1SJ71QLP21	4	Q2ASCPU-S1	4
A1SJ71QC24	4	A1SJ71QE71N2-B2	3	A1SJ71QLP21GE	4	Q2ASHCPU	4
A1SJ71QC24N	4	A1SJ71QE71N2-B5T	3	A1SJ71QLP21S	4	Q2ASHCPU-S1	4
A1SJ71QC24N1	4	A1SJ71QE71N3-T	4	A1SJ71QLR21	4	Q2ASHCPU-S124	4
A1SJ71QC24N1-R2	4	A1SJ71QE71N-B2	4	A1SJ72QBR15	4		
A1SJ71QC24N-R2	4	A1SJ71QE71N-B5	4	A1SJ72QLP25	4		

The conformity of the above mentioned products with the regulations of the directive 89/336/EEC for Electro-magnetic Compatibility is shown by the direct application of product tests to the following EMC standards directly and indirectly (when Generic EMC standards are used).

## Harmonised European Standards

2001

Reference No.	Date of Issue
EN50081-2	1993
prEN50082-2	1992
EN50082-2	1995
EN61131-2	1995
EN61131/A	2000

Modules marked with a mark 1 have been tested to EN50081-2 and prEN50082-2 Modules marked with a mark 2 have been tested to EN50081-2 and EN50082-2. Modules marked with a mark 3 have been tested to EN61131-2, EN50081-2 and EN50082-2. Modules marked with a mark 4 have been tested to EN61131-2/A12(2000) and EN61000-6-4..

Revision record

0

EN61000-6-4

22/06/05 A1S38HBEU,Q2ASCPU,Q2ASCPU-S1,Q2ASHCPU-S124 revised according to the harmonized rule revision.

P 27/06/05 A1SJ61QBT11,A1SJ71QBR11,A1SJ71QC24,A1SJ71QC24N,A1SJ71QC24N-R2,A1SJ71QC24-R 2,A1SJ71QLP21GE,A1SJ71QLR21,A1SJ72QBR15,A1SJ72QLP25,A1SJ72QLR25 revised according to the harmonized rule revision.

10/11/05 A1SJ71QC24N1,A1SJ71QC24N1-R2 added to the list.