

### Mitsubishi Programmable Controller

## MELSEC iQ-R

MELSEC iQ-R CPU Module Function Block Reference

### CONTENTS

СНА	PTER 1	FUNCTION BLOCK (FB) LIST	2
СНА	PTER 2	CPU MODULE FB	4
2.1	M+RCPU	_MSynchronization_Delay1OUT, M+RCPU_MSynchronization_Delay2OUT	4
2.2	M+RCPU	_MSynchronization_Delay1SET, M+RCPU_MSynchronization_Delay2SET	6
2.3	M+RCPU	_MSynchronization_Delay1RST, M+RCPU_MSynchronization_Delay2RST	8
2.4	M+RCPU	_MSynchronization_Delay1MOV, M+RCPU_MSynchronization_Delay2MOV	10
2.5	M+RCPU	_MSynchronization_Delay1DMOV, M+RCPU_MSynchronization_Delay2DMOV	12
INST	RUCTIC	N INDEX	14
REVIS	SIONS		16

# **1** FUNCTION BLOCK (FB) LIST

This chapter lists the FBs for the MELSEC iQ-R series CPU module.

#### FBs for the Ethernet function

For the Ethernet function FBs of CPU modules, refer to the "Ethernet-EQUIPPED MODULE FB" in the following. MELSEC iQ-R Ethernet/CC-Link IE Function Block Reference

#### FBs for the inter-module synchronization function

	-
Name <sup>*1</sup>	Description
M+RCPU_MSynchronization_Delay1OUT	M+RCPU_MSynchronization_Delay1OUT holds the value specified by input data (output request data) in the FB and executes the OUT instruction when the FB is called next.
M+RCPU_MSynchronization_Delay2OUT	M+RCPU_MSynchronization_Delay2OUT executes the OUT instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1SET	M+RCPU_MSynchronization_Delay1SET memorizes that this function has been called by the FB and executes the SET instruction when the FB is called next.
M+RCPU_MSynchronization_Delay2SET	M+RCPU_MSynchronization_Delay2SET executes the SET instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1RST	M+RCPU_MSynchronization_Delay1RST memorizes that this function has been called by the FB and executes the RST instruction when the FB is called next.
M+RCPU_MSynchronization_Delay2RST	M+RCPU_MSynchronization_Delay2RST executes the RST instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1MOV	M+RCPU_MSynchronization_Delay1MOV holds the value specified by input data in the FB and executes the MOV instruction when the FB is called next.
M+RCPU_MSynchronization_Delay2MOV	M+RCPU_MSynchronization_Delay2MOV executes the MOV instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1DMOV	M+RCPU_MSynchronization_Delay1DMOV holds the value specified by input data in the FB and executes the DMOV instruction when the FB is called next.
M+RCPU_MSynchronization_Delay2DMOV	M+RCPU_MSynchronization_Delay2DMOV executes the DMOV instruction when the FB is called second time.

\*1 Note that this reference does not describe the FB version information which is displayed such as "\_00A" at the end of FB name

# 2 CPU MODULE FB

### 2.1 M+RCPU\_MSynchronization\_Delay1OUT, M+RCPU\_MSynchronization\_Delay2OUT

#### Name

4

M+RCPU\_MSynchronization\_Delay1OUT, M+RCPU\_MSynchronization\_Delay2OUT

FB details		
Item	Description	
Overview	Holds the value specified by output next or the second time.	request data (input data) in the FB and executes the OUT instruction when the FB is called
Symbol	Output request B: i_bD	M+RCPU_MSynchronization_Delay1OUT ata o_bData: B Output data
	Output request B: i_bD	M+RCPU_MSynchronization_Delay2OUT ata o_bData: B Output data
Available device	CPU module	RCPU
	Engineering tool	GX Works3
Language	Ladder diagram	
Number of basic steps	12 steps	
Processing	second time. M+RCPU_MSynchronization_Delay	pecified by i_bData in the FB and outputs it to o_bData when the FB is called next or the 10UT outputs data when the FB is called next. 20UT outputs data when the FB is called second time.
FB compilation method	Macro type	
FB operation	Always executed	
Input condition for FB_EN	None	
Timing chart of I/O signals	M+RCPU_MSynchronization_Dela  i_bData [Input data]  o_bData [Output data]  FB call timing FI	ay10UT B call timing FB call timing
	* M+RCPU_MSynchronization_De	elay2OUT outputs data at the second FB call timing after FB is once called.
Restrictions or precautions	Always outputs OFF by the numb	er of Delavs after STOP changes to RUN.

There is no error code.

#### Labels

#### ∎Input label

	Name	Variable name	Data type	Range	Default value	Description
Ì	Output request data	i_bData	Bit	On or off	None	Specifies output data. On: Requesting output on Off: Requesting output off

Name	Variable name	Data type	Range	Description
Output data	o_bData	Bit	On or off	Output data are set.

### 2.2 M+RCPU\_MSynchronization\_Delay1SET, M+RCPU\_MSynchronization\_Delay2SET

#### Name

M+RCPU\_MSynchronization\_Delay1SET, M+RCPU\_MSynchronization\_Delay2SET

FB details					
Item	Description				
Overview	Memorizes that this second time.	function has been o	called by the FB and executes the SET	instruction wh	en the FB is called next or the
Symbol	Execution command	M+F - B: i_bEN	RCPU_MSynchronization_Delay1SET	o_bData: B	Output data
	Execution command	M+F B: i_bEN	RCPU_MSynchronization_Delay2SET	o_bData: B	Output data
Available device	CPU module		RCPU		
	Engineering tool		GX Works3		
Language	Ladder diagram				
Number of basic steps	12 steps				
Processing	time. M+RCPU_MSynchi	ronization_Delay1SE	olds the state in the FB and sets it in o_ ET is set when the FB is called next. ET is set when the FB is called second	-	ne FB is called next or the second
FB compilation method	Macro type				
FB operation	Always executed				
Input condition for FB_EN	None				
Timing chart of I/O signals	i_bEN [Execution command] o_bData [Output data]	-	SET	3 call timing af	er FB is
Restrictions or precautions	Holds the output	by the number of De	elays after STOP changes to RUN.		

There is no error code.

#### Labels

#### ∎Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.

Name	Variable name	Data type	Range	Description
Output data	o_bData	Bit	On or off	Output data are set.

### 2.3 M+RCPU\_MSynchronization\_Delay1RST, M+RCPU\_MSynchronization\_Delay2RST

#### Name

M+RCPU\_MSynchronization\_Delay1RST, M+RCPU\_MSynchronization\_Delay2RST

FB details					
Item	Description				
Overview	Memorizes that this second time.	function has been o	called by the FB and executes the RST	instruction wh	en the FB is called next or the
Symbol	Execution command	M+RCPU_MSynchronization_Delay1RS B: i_bEN		o_bData: B	——— Output data
	Execution command	M+F B: i_bEN	RCPU_MSynchronization_Delay2RST	o_bData: B	Output data
Available device	CPU module		RCPU		
	Engineering tool		GX Works3		
Language	Ladder diagram		·		
Number of basic steps	12 steps				
Processing	time. M+RCPU_MSynchi	onization_Delay1R	nolds the state in the FB and resets o_ ST resets the device when the FB is ca ST resets the device when the FB is ca	lled next.	
FB compilation method	Macro type				
FB operation	Always executed				
Input condition for FB_EN	None				
Timing chart of I/O signals	i_bEN [Execution command] o_bData [Output data]	-	RST	FB call timing a	— — after FB is
Restrictions or precautions	Holds the output	by the number of De	elays after STOP changes to RUN.		

There is no error code.

#### Labels

#### ∎Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.

Name	Variable name	Data type	Range	Description
Output data	o_bData	Bit	On or off	Output data are set.

### 2.4 M+RCPU\_MSynchronization\_Delay1MOV, M+RCPU\_MSynchronization\_Delay2MOV

#### Name

M+RCPU\_MSynchronization\_Delay1MOV, M+RCPU\_MSynchronization\_Delay2MOV

FB details	B details						
Item	Description						
Overview	Holds the value specified by tran called next or the second time.	sfer source data (input data) in the FB and executes the MOV instruc	tion when the FB is				
Symbol	Execution command B: i_bl Transfer source data W: i_v		Transfer destination data				
	Execution B: i_b command Transfer W: i_v		Transfer destination data				
Available device	CPU module	RCPU					
	Engineering tool	GX Works3					
Language	Ladder diagram						
Number of basic steps	<ul> <li>18 steps (M+RCPU_MSynchromagnetic content of the steps (M+RCPU_MSynchromagnetic content of the steps of the steps</li></ul>						
Processing	the FB is called next or the secon M+RCPU_MSynchronization_De	When i_bEN is turned on, the function transfers the data specified by i_wData to o_wData in 16-bit data transfer mode when the FB is called next or the second time. M+RCPU_MSynchronization_Delay1MOV transfers data when the FB is called next. M+RCPU_MSynchronization_Delay2MOV transfers data when the FB is called second time.					
FB compilation method	Macro type	-					
FB operation	Always executed						
Input condition for FB_EN	None						
Timing chart of I/O signals	M+RCPU_MSynchronization i_bEN [Execution command] i_wData [Transfer source data] o_wData [Transfer destination data] FB call timing	_Delay1MOV					
	* M+RCPU_MSynchronization	_Delay2DMOV transfers data at the second FB calling after FB is on	ce called.				
Restrictions or precautions	Holds the output by the number	er of Delays after STOP changes to RUN.					

There is no error code.

#### Labels

#### ∎Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.
Transfer source data	i_wData	Word [signed]	Effective device range	None	Specify the transfer source data or the device containing the transfer source data.

Name	Variable name	Data type	Range	Description
Transfer destination data	o_wData	Word [signed]	Effective device range	Transfer data is stored.

### 2.5 M+RCPU\_MSynchronization\_Delay1DMOV, M+RCPU\_MSynchronization\_Delay2DMOV

#### Name

M+RCPU\_MSynchronization\_Delay1DMOV, M+RCPU\_MSynchronization\_Delay2DMOV

FB details				
Item	Description			
Overview	Holds the value specified by transfer source data (input data) in the FB and executes the DMOV instruction when the FB called next or the second time.			
Symbol	Execution B: i_bEN command Transfer D: i_dData	M+RCPU_MSynchronization_Delay1DMOV o_dData: D	Transfer destination data	
	Execution B: i_bEN command Transfer D: i_dData	M+RCPU_MSynchronization_Delay2DMOV o_dData: D	Transfer destination data	
Available device	CPU module Engineering tool	RCPU GX Works3		
Language	Ladder diagram			
Number of basic steps Processing	<ul> <li>18 steps (M+RCPU_MSynchronization_Delay1DMOV)</li> <li>20 steps (M+RCPU_MSynchronization_Delay2DMOV)</li> <li>When i_bEN is turned on, the function transfers the data specified by i_dData to o_dData in 32-bit data transfer mode when th FB is called next or the second time.</li> <li>M+RCPU_MSynchronization_Delay1DMOV transfers data when the FB is called next.</li> <li>M+RCPU_MSynchronization_Delay2DMOV transfers data when the FB is called second time.</li> </ul>			
FB compilation method	Macro type			
FB operation	Always executed			
Input condition for FB_EN	None			
Timing chart of I/O signals		1DMOV B C C C C C C C C C C C C C C C C C C	се	
Restrictions or precautions	Holds the output by the number of D	elays after STOP changes to RUN.		

There is no error code.

#### Labels

#### ∎Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.
Transfer source data	i_dData	Double word [signed]	Effective device range	None	Specify the transfer source data or the device containing the transfer source data.

Name	Variable name	Data type	Range	Description
Transfer destination data	o_dData	Double word [signed]	Effective device range	Transfer data is stored.

### **INSTRUCTION INDEX**

### М

Л	
M+RCPU_MSynchronization_Delay1DMOV 12	
M+RCPU_MSynchronization_Delay1MOV 10	
M+RCPU_MSynchronization_Delay1OUT 4	
M+RCPU_MSynchronization_Delay1RST 8	
M+RCPU_MSynchronization_Delay1SET 6	
M+RCPU_MSynchronization_Delay2DMOV 12	
M+RCPU_MSynchronization_Delay2MOV 10	
M+RCPU_MSynchronization_Delay2OUT 4	
M+RCPU_MSynchronization_Delay2RST 8	
M+RCPU_MSynchronization_Delay2SET 6	

### REVISIONS

\*The manual number is given on the bottom left of the back cover.

Revision date	*Manual number	Description		
June 2014	BCN-P5999-0374-A	First edition		

Japanese manual number: BCN-P5999-0364-A

This manual confers no industrial property rights of any other kind, nor does it confer any patent licenses. Mitsubishi Electric Corporation cannot be held responsible for any problems involving industrial property rights which may occur as a result of using the contents noted in this manual.

© 2014 MITSUBISHI ELECTRIC CORPORATION

BCN-P5999-0374-A(1406)MEE

### MITSUBISHI ELECTRIC CORPORATION

HEAD OFFICE : TOKYO BUILDING, 2-7-3 MARUNOUCHI, CHIYODA-KU, TOKYO 100-8310, JAPAN NAGOYA WORKS : 1-14 , YADA-MINAMI 5-CHOME , HIGASHI-KU, NAGOYA , JAPAN

When exported from Japan, this manual does not require application to the Ministry of Economy, Trade and Industry for service transaction permission.

Specifications subject to change without notice.